



AVIT



AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY

An Ambit Institution of

VINAYAKA MISSION'S RESEARCH FOUNDATION

(Deemed to be University) (Declared under sec3 of UGC Act 1956)



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Department of Electrical and Electronics Engineering

Cordially invites you to the

**SKILL DEVELOPMENT TRAINING ON
“DESIGN AND FABRICATION OF LOW COST UPS
AND INVERTER FOR RURAL COMMUNITY”**

Date : 21st & 22nd Dec 2018

Time: 10.00 AM

Venue: Digital Hall 2

Inaugural Address

Sir Dr. M. Ponnavaikko

Provost, VMRF

Valedictory Address

Dr.S.A.V.Satya Murty

Director (ETR), VMRF

Felicitation

Dr. P. Shankar

Principal, AVIT

Mr. T. Srinivasan
Head Operations, AVIT

Prof. D. Vijendra Babu
VP(Academics), AVIT

Prof. P. Rajasekaran
VP(Admin), AVIT

Resource Persons

Dr. G. Ezhilarasan
Prof, EEE, AVIT

Dr. K. Boopathy
Asso. Prof, EEE, AVIT

With Warm Regards : Prof. L.Chitra, HOD, EEE, AVIT