



**AVIT**  
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S  
RESEARCH FOUNDATION  
(Deemed to be University under section 3 of the UGC Act 1956)



Accredited by NAAC



Approved by AICTE

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

Name of the Lab: 17ECCC82 – Digital Logic Circuits & Design Lab

**BATCH DETAILS**

S. NO.	BATCH NO	REGISTER NO.	NAME OF THE STUDENT
1	I	3522010501	MOHAMMED ABBAS S B
2		3522010502	SHUBHAM SAURABH
3		3522120501	ASWIN PRINCE BALAN H
4		3522120502	GANESH KUMAR P
5	II	3522120503	MANOBALA M K
6		3522120504	VISHVESWARAN V
7		3522120505	YOGESH S

*LC&E*