



VINAYAKA MISSION'S RESEARCH FOUNDATION
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY, PAIYANOOR
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



STANDARD OPERATING PROCEDURE

Name of the Lab./facility	MICRO PROCESSOR AND MICRO CONTROLLER LAB
Name of the equipment	DIGITAL IC TRAINER KIT
Purpose	TO PERFORM VARIOUS DIGITAL LOGIC OPERATIONS
Scope	VARIOUS LOGICAL CIRCUITS CAN BE DESIGNED AND THE OPERATION CAN BE CHECKED
Responsibility	N. P. GOPINATH

STANDARD OPERATING PROCEDURE

- The connections should be given as per the experiment to be performed referring to the lab.

ALL EXPERIMENTS

- Connections thus given shall be verified by the course instructor or lab in-charge
- To perform the experiment supply mains have to be switched on.
- Power supply to the digital IC trainer to be switched on.
- Connect the terminals as per the logic.

RECORD TO BE MAINTAINED

- Laboratory Manual containing the experiments that can be performed with the equipment
- Maintenance Record