



AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S
RESEARCH FOUNDATION
(Deemed to be University under section 3 of the UGC Act 1956)



Accredited by NAAC



Approved by AICTE

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Name of the Course : SEMICONDUCTOR DEVICES AND CIRCUITS LAB

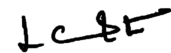
Year / Sem : II/III

Branch : EEE

LIST OF EXPERIMENTS

1. To study experimentally the characteristics of Diodes, BJT's and FET's.
2. To plot the input and output characteristics of a transistor in CE Configuration and to compute the h-parameters
3. To study Drain characteristics and Transfer characteristics & to find the Transconductance, Drain resistance and Amplification factor of JFET
4. Simulation & Hardware realization of Half wave & Full wave Rectifier with and without Filter.
5. Simulation & Hardware realization of Clipping & Clamping circuits..
6. Simulation & Hardware realization of Voltage Series Feedback amplifiers and its frequency analysis
7. Design, Simulation and Hardware realization of Sinusoidal waveform generators.
 - a. RC Oscillators
 - b. LC Oscillators
8. Design and simulation of Power amplifiers
9. Frequency Response characterization of Single Tuned amplifier circuit.
10. Miniproject

K.S. KAVITHA KUMARI
Faculty In-charge


HOD/EEE