

**VINAYAKA MISSIONS RESEARCH FOUNDATION
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY, PAIYANOOR
TIME TABLE
Ref No.AVIT/TT/03**

SEMESTER: ODD
DEPARTMENT:EEE
Venue: Circuits and Devices Lab
Lab Head: Ms.Jensie Anita/AP-I

Academic Year 2021-2022
Date: 26/08/2021
With effort from :06/09/2021

Day/ Period	1	2	MORNING BREAK (11:00A.M -11:15A.M)	3	LUNCH BREAK (12.15 P.M. - 1.00 P.M.)	4	5	AFTERNOON BREAK (2:55 P.M -3.05 P.M)	6
	9:00- 10.00	10.00- 11.00		11.15- 12.15		1:00- 2:00	2.00 - 2.55		3.05-4.00
Monday							SDC LAB SD LAB		SDC LAB SD LAB
Tuesday									
Wednesday									
Thursday							LIC LAB		LIC LAB
Friday									

S.NO	MNEMONIC	COURSE CODE	COURSE TITLE	Sem. / Branch	NAME OF THE FACULTY
1	LIC LAB	17ECCC94	Linear Integrated Circuits Lab	V/EEE	KAVITHA KUMARI.K.S /AP-II/EEE A.DIVYA/LAB ASSIST
2	SDC LAB	17ECCC93	Semiconductor Devices and circuits lab	II/EEE	KAVITHA KUMARI.K.S /AP-II/EEE M.JAYARAMAN/LAB ASSIST
3	SD LAB	17ECCC81	Semiconductor Devices lab	III/MECT	P.POORNIMA/AP-II/EEE M.JAYARAMAN/LAB ASSIST

V. RATTAN KUMAR
TIME TABLE CO-ORDINATOR

Jensie Anita

HOD

