



**AVIT**  
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S  
RESEARCH FOUNDATION  
(Deemed to be University under section 3 of the UGC Act 1956)



Accredited by NAAC



Approved by AICTE

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Name of the Course : LINEAR INTEGRATED CIRCUITS LAB

Year / Sem : III/V

Branch : EEE

### LIST OF EXPERIMENTS

1. Design of Inverting and Non-Inverting amplifier using operational amplifier.
2. Design of Adders and Subtractors using operational amplifier.
3. Design of Integrators and Differentiators using operational amplifiers.
4. Design of comparators using operational amplifiers.
5. Design of rectifiers using operational amplifiers.
6. Design of oscillators using operational amplifier.
7. Design of Astable and Monostable Multivibrators using IC555 Timer
8. Design of filters using operational amplifier.
9. Design of Digital to analog converter and Analog to Digital converters.
10. Design and implementation of Phase Locked Loops.

HOD/ EEE  
Dr. L. Chitra