



AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S
RESEARCH FOUNDATION
(Deemed to be University under section 3 of the UGC Act 1956)



Accredited by NAAC



Approved by AICTE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

LIST OF EXPERIMENTS

PROG, BRANCH,	B. E., E. C. E & B.M.E
YEAR/ SEMESTER, SECTION	II / III, -
SUBJECT	17ECCC83_ ANALOG CIRCUITS LAB
ACADEMIC YEAR	2021-2022 (ODD SEMESTER)

1. Design, Simulation and Hardware realization of Single Stage Common Emitter amplifier for given specification
2. Simulation & Hardware realization of Feedback amplifiers and its frequency analysis
 - a) Voltage Series
 - b) Current Shunt
3. Design, Simulation and Hardware realization of Sinusoidal waveform generators.
 - a) RC Oscillators
 - b) LC Oscillators
4. Design and simulation of Power amplifiers
5. Frequency Response characterization of Tuned amplifier circuit.
 - a) Single Tuned
 - b) Double Tuned
6. Design and hardware realization of Multistage Amplifier for given specification
 - a) Cascade
 - b) Darlington
7. Design and simulation of Differential pair circuit with active load and current references and its frequency analysis.
8. Miniproject.

HOD/ ECE