



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PROG, BRANCH,	B. E., E. C. E
YEAR, SEMESTER, SECTION	II / III, -
SUBJECT	17ECCC82 - DIGITAL LOGIC CIRCUITS & DESIGN LAB
ACADEMIC YEAR	2021-2022 (ODD SEMESTER)

STANDARD OPERATING PROCEDURE

Name of the Lab./facility	ELECTRONICS LAB I
Purpose	To provide training for students, research scholars and industrial personnel, in implementing many application using logic gates. Using this digital trainer, the student is exposed to basic electronic theory of power supplies, regulation, ripple filtering, oscillators, multivibrator, rectifiers, diodes and transistor amplifiers. After this basic course, the student proceeds to an understanding of digital electronics.
Scope	The objective for this lab is to understand the fundamentals of logic gates and its use in implementing and testing basic Boolean functions. The student can implement digital logic concept in their projects.
Responsibility	Faculty In-charge of the facility, HOD/ECE
STANDARD OPERATING PROCEDURE FOR DIGITAL IC TRAINER KIT	
<ul style="list-style-type: none"> • The connections should be given as per the experiment to be performed referring to the lab. Manual. • Connections thus given shall be verified by the course instructor or lab in-charge • To perform the experiment supply mains have to be switched on. • Upon completion of experiment the Digital Trainer IC Kit shall be turned off first, followed by turning off of mains power supply. 	

[Handwritten Signature]

HOD / ECE

PRECAUTIONS TO BE FOLLOWED

- Short circuit of the battery terminals or any source terminals has to be avoided.
- Avoid shorting circuit the output of DC power supply.
- Set the voltage and current adjustment knobs as you desire. The unit should be stored in a dry and well ventilated place and the power cord removed if storing for long periods.
- Turn off the DC Power supply and disconnect the connection after completion of experiment.



HOD / ECE