



AVIT
AAKUPADAI VREEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S
RESEARCH FOUNDATION
(Governing body established by section 3 of the MGC Act 1952)



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Department of Electrical and Electronics Engineering

LIST OF EXPERIMENTS

1. PLC ladder diagram using logic gates.
2. Stepper Motor interface.
3. D.C motor controller interface.
4. Linear actuation of hydraulic cylinder with counter and speed control.
5. Sequential operation of pneumatic cylinders.
6. Traffic light controller.
7. Speed control of DC motor using PLC.
8. Testing of Relays using PLC.
9. Design of ON / OFF control in Flow and Pressure process control in PLC.

LCSE